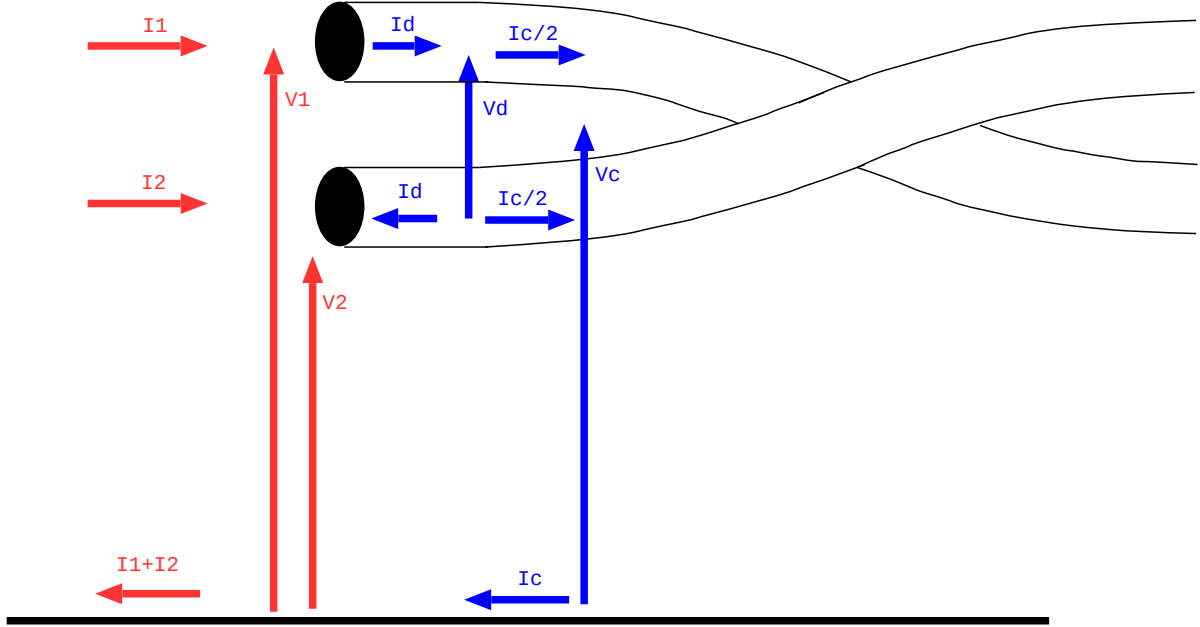
I1
Id
Ic/2
V1
Vd
I2
Id
Ic/2
Vc
V2
I1+I2
Ic